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PATENTS  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant : Glenn J. Leedy

Application No. : 10/672,961 Confirmation No.: 9439

Filed : September 26, 2003

For : THREE DIMENSIONAL MULTI LAYER MEMORY AND  
CONTROL LOGIC INTEGRATED CIRCUIT STRUCTURE

Art Unit : 2822

Examiner : Monica Lewis

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

REPLY TO OFFICE ACTION

Sir:

In reply to the January 29, 2007 Office Action,  
applicant hereby amends the above-identified patent application as  
follows:

Amendments to the Claims begin on page 2 of this Reply.

Remarks begin on page 12 of this Reply.